

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|--|------------------|---------|---------------------|
| L1 | 159 | array near5 processor same asynchronous | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:19 |
| L2 | 1 | array near5 processor same asynchronous with "processing elements" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:19 |
| L3 | 21 | "712".clas. array near5 processor same asynchronous same "processing elements" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:20 |
| L4 | 861 | array near5 processor same "flash" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:37 |
| L5 | 620 | array near5 processor same "flash memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:37 |
| L6 | 0 | array near2 processor same "flash memory" same "processor elements" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:37 |
| L7 | 2 | "712".clas. array near2 processor same "flash memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:37 |
| L8 | 3 | "712".clas. array near2 processor same "flash" with memory | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:38 |
| L9 | 9 | "712".clas. array near5 processor same "flash" with memory | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:39 |
| L10 | 734 | "712".clas. ROM near5 "flash memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:40 |
| L11 | 54 | "712".clas. ROM near5 "flash memory" with instructions | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/04 13:40 |

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|-----|------|---|--|-----|-----|---------------------|
| S1 | 1 | "5784584".pn. | US-PGPUB; USPAT | OR | ON | 2006/04/26 11:34 |
| S3 | 188 | 712/10.ccls. | US-PGPUB; USPAT | OR | ON | 2006/04/27 10:44 |
| S4 | 0 | S3 and "sub-task".ab. | US-PGPUB; USPAT | OR | ON | 2006/04/27 10:44 |
| S5 | 1 | S3 and "subtask".ab. | US-PGPUB; USPAT | OR | ON | 2006/04/27 11:00 |
| S6 | 14 | S3 and "task".ab. | US-PGPUB; USPAT | OR | ON | 2006/04/27 11:00 |
| S7 | 1370 | 712/10-22.ccls. | US-PGPUB; USPAT | OR | ON | 2006/04/27 13:20 |
| S8 | 17 | S7 and (gps or "global positioning system") | US-PGPUB; USPAT | OR | ON | 2006/04/27 13:21 |
| S9 | 568 | (712/10.ccls. or 712/11.ccls.) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/11 11:51 |
| S11 | 66 | (712/10.ccls. or 712/11.ccls.) asynchronous direct\$4 near5 connect\$4 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/11 13:58 |
| S12 | 5 | (712/10.ccls. or 712/11.ccls.) asynchronous near5 communicat\$4 same (processor\$1 or "array elements" or computers or nodes) direct\$4 near5 connect \$4 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/11 14:00 |
| S13 | 66 | (712/10.ccls. or 712/11.ccls.) asynchronous direct\$4 near5 connect\$4 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/11 14:05 |
| S14 | 0 | (712/10.ccls. or 712/11.ccls.) asynchronous direct\$4 near5 connect\$4 "dedicated paths" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/11 14:05 |
| S15 | 1 | (712/10.ccls. or 712/11.ccls.) asynchronous direct\$4 near5 connect\$4 same dedicated | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 09:59 |

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| S16 | 1 | (712/10.ccls. or 712/11.ccls.) asynchronous direct\$4 near5 connect\$4 same dedicat\$2 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 10:00 |
| S17 | 219 | (712/10.ccls. or 712/11.ccls.) dedicated | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 10:00 |
| S18 | 90 | (712/10.ccls. or 712/11.ccls.) dedicated near5 (line \$1 or bus\$2 or path\$1 or communicat\$4 or wire\$1) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 10:58 |
| S19 | 1 | (712/10.ccls. or 712/11.ccls.) ("5X5" or "5 by 5") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 10:59 |
| S20 | 568 | (712/10.ccls. or 712/11.ccls.) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:00 |
| S21 | 397 | (712/10.ccls. or 712/11.ccls.) array | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:01 |
| S22 | 178 | (712/10.ccls. or 712/11.ccls.) (array or matrix) with processors | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:18 |
| S23 | 0 | (712/10.ccls. or 712/11.ccls.) ("4 by 6" or "6 by 4" or "four by six" or "six by four") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:04 |
| S24 | 253 | (712/10.ccls. or 712/11.ccls.) (array or matrix) number near5 (processors or elements) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:18 |
| S25 | 5 | (712/10.ccls. or 712/11.ccls.) (array or matrix) number near5 (processors or elements) same obvious | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:19 |

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| S26 | 150 | (712/10.ccls. or 712/11.ccls.) (array or matrix) same number near5 (processors or elements) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:37 |
| S27 | 1 | (712/10.ccls. or 712/11.ccls.) ("4.times.6" or "6.times.4") ("5.times.5") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:38 |
| S28 | 3 | (712/10.ccls. or 712/11.ccls.) ("4.times.6" or "6.times.4") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:41 |
| S29 | 19 | (712/10.ccls. or 712/11.ccls.) "5.times.5" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:45 |
| S30 | 598 | array with processor\$1 "5.times.5" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:45 |
| S31 | 32 | array with processor\$1 same "5.times.5" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:45 |
| S32 | 6 | "712".clas. array with processor\$1 same "5.times.5" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:46 |
| S33 | 113 | (712/10.ccls. or 712/11.ccls.) asynchronous | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:54 |
| S34 | 15 | (712/10.ccls. or 712/11.ccls.) asynchronous with processors | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/16 11:54 |
| S35 | 2 | "6154809".pn. | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 09:30 |
| S36 | 2 | "6085304".pn. | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 09:30 |
| S37 | 2 | "6002393".pn. | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 11:05 |

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| S38 | 70 | array with processors same ROM same RAM | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 11:06 |
| S39 | 3 | "712".clas. array with processors same ROM same RAM | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 11:07 |
| S40 | 6 | "712".clas. (array or matrix or mesh) with (processors or computers or "processing elements") same ROM same RAM | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 11:08 |
| S41 | 14 | "712".clas. dedicated with ROM with RAM | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2007/01/17 11:08 |
| S42 | 26 | "712".clas. matri\$4 with reflect\$4 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/05/14 13:42 |
| S43 | 8 | "712".clas. matri\$4 with reflect\$4 same row\$2 same column\$2 | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/05/14 13:43 |
| S44 | 2 | "20040107332" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 15:16 |
| S45 | 254 | 712/10.ccls. | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 18:28 |
| S46 | 42 | 712/10.ccls. asynchronous | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 18:28 |
| S47 | 14 | 712/10.ccls. "dedicated memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 18:29 |
| S48 | 2 | 712/10.ccls. "dedicated memory" same (ram or "random access memory") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 18:30 |

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| S49 | 123 | 712/10.ccls. (ram or "random access memory") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 18:30 |
| S50 | 8 | 712/10.ccls. (ram or "random access memory") with "processing element" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 18:31 |
| S51 | 0 | 712/10.ccls. "dedicated memory" with "data memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:33 |
| S52 | 0 | 712/10.ccls. "dedicated memory" same "data memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:33 |
| S53 | 0 | 712/10.ccls. "dedicated data memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:34 |
| S54 | 14 | 712/10.ccls. ("processing element" or PE) with "data memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:34 |
| S55 | 26 | 712/10.ccls. ("processing element" or PE) with ("data memory" or RAM or "random access memory") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:34 |
| S56 | 20 | 712/10.ccls. (PE or "processing element") with "local memory" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:42 |
| S57 | 2 | 712/10.ccls. (PE or "processing element") with "local memory" same (RAM or "random access memory") | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:42 |
| S58 | 17 | 712/10.ccls. (PE or "processing element") with "local memory" (array or matrix) with (processor or processing) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:45 |

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| S59 | 1 | 712/10.ccls. (PE or "processing element") with "local data memory" (array or matrix) with (processor or processing) | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 19:46 |
| S60 | 13 | 712/10.ccls. (array or matrix) near5 "integrated circuit" | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 20:00 |
| S61 | 3 | 712/10.ccls. (array or matrix) with integrated with substrate | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2008/06/15 20:02 |
| S62 | 885 | ("processor element" or PE) same RAM same ROM | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/03 13:57 |
| S63 | 22 | ("processor element" or PE) same RAM same ROM same array | US-PGPUB; USPAT; USOCR; DERWENT | AND | OFF | 2009/03/03 13:58 |

3/ 4/ 2009 1:52:39 PM

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